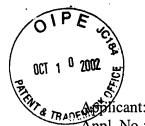
2451 # S et No. 0 271 10-18 02

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))	Docket No. 112740-271 <i>し</i> ワー/タ
In Re Application Of Gerhand Spitz	
Serial No. Filing Date Examiner  09/890,432  Filing Date Examiner  July 30, 2001	Group Art Unit 2151
Title: A METHOD FOR SECURING ACCESS TO AT LEAST ONE VARIABLE IN MULTITASKING CONTROLLED PROCESSOR SYSTEM	
Address to: Assistant Commissioner for Patents Washington, D.C. 20231	OCT 1,5 2002
37 CFR 1.97(b)	echnology Center 2100
The Information Disclosure Statement submitted herewith is being filed with of a national application other than a continued prosecution application ur three months of the date of entry of the national stage as set forth in 37 C application; before the mailing of a first Office Action on the merits, or before Action after the filing of a request for continued examination under 37 CFR 1	nder 37 CFR 1.53(d); within FR 1.491 in an international to the mailing of a first Office
37 CFR 1.97(c)	
2.  The Information Disclosure Statement submitted herewith is being filed after CFR 1.97(b), provided that the Information Disclosure Statement is filed to Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFI otherwise closes prosecution in the application, and is accompanied by one	efore the mailing date of a R 1.311, or an Action that
☐ the statement specified in 37 CFR 1.97(e);	
OR	
☐ the fee set forth in 37 CFR 1.17(p).	SCHOOL STATES

TRANSMITTAL OF INFORMATION DISCLO (Under 37 CFR 1.97(b) or 1.97	Docket No. 112740-271	
In Re Application: Gerhard Spitz	,	
Serial No. Filing Date 09/890,432 PANEMARY July 30, 2001	Examiner	Group Art Unit 2151
A METHOD FOR SECURING ACCESS TO AT LI MULTITASKING CONTROLLED PROCESSOR SYSTE		EEMPTIVELY
·	nent of Fee ects to pay the fee set forth in 37 CFR	: 1.17(p))
☐ A check in the amount of is atta  ☐ The Assistant Commissioner is hereby authorized as described below. A duplicate copy of this shee ☐ Charge the amount of ☐ Credit any overpayment. ☐ Charge any additional fee required. ☐ Certificate of Transmission by Facsimile*  ☐ I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (F ☐ (Date)  ☐ Signature	Certificate of Mailing  I certify that this pocument ar October 7, 2017 as first cass mail under endressed to the Assistant Co	RECEIVED OCT 1 5 2002 by Fir <b>Technology</b> Center 210
*This certificate may only be used if paying by deposit account.	Typed or Printed Name of Pe	erson Mailing Certificate
William E. Vaughan (Reg. No. 39,056) Bell, Boyd & Lloyd LLC P.O. B x 1135 Chicago, Illinois 60690  CC:		



### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.:

Gerhard Spitz 09/890,432

Filed:

July 30, 2001

Title:

A METHOD FOR SECURING ACCESS TO AT LEAST ONE VARIABLE IN

A PREEMPTIVELY MULTITASKING CONTROLLED PROCESSOR

**SYSTEM** 

Art Unit:

2151

Examiner:

Docket No.:

112740-271

**Assistant Commissioner for Patents** Washington, DC 20231

Technology Center 2100

#### INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 37 C.F.R. 1.97, and 37 C.F.R. 1.98, Applicant requests that a citation and examination of the references cited below, and on the attached PTO-1449 form, copies of which are enclosed, be made during the course of examination of the above-identified application for United States patent.

## **U.S. PATENT DOCUMENTS**

Document No.

Date

Inventor

4,638,426

1-20-87

Chang et al.

#### **FOREIGN PATENT DOCUMENTS**

Document No.

Date

Country

DE 38 51 038

10-20-88

Germany

WO 98/33119

7-30-98

**PCT** 

#### **OTHER DOCUMENTS**

Microsoft Knowledge Base Article – Q125867, "Understanding Win 16Mutex" 07/02/95 pages 1-2

M. Pietrek; Direct Thunking in Windows 95, Dr. Dobb's Journal, 1/96

"Architektur von Betriebssystemen", Wetterstein, Hanser Studien Bucher, 1984, page 54 ff

Andrew Tananbaum, "Betriebssysteme - Entwurf und Realisierung" Teil 1, Prentice Hall International 1990 pages 31-32

German DE 38 51 038 relates to a protected memory accessing method using a virtual block addressing and an address translation at a memory controller via an access table.

Applicant looks forward to early and favorable consideration of this matter.

Respectfully submitted,

BELL, BOYD & LLOYD LLC

BY

William E. Vaughar

Reg. No. 39,056 P.O. Box 1135

Chicago, Illinois 60690-1135

Phone: (312) 807-4292

# OIPE

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

PTO	Form	1449
rio	LOIII	1447

Atty Docket No.	Application No.
112740-271	09/890,432
Applicant	
Spitz	
Filing Date	Group
7-30-01	1

		U.S. PA	TENT DOCUMENT	S		
Examiner's Initials	Document Number	Publication Date	Inventor	Class	Subclass	Filing Date If Appropriate
	4,638,426	1-20-87	Chang et al.			
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				,,,,	ECEN	
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Examiner's	Document	Publication			T .	Translation	
Initials	Number	Date	Country	Class	Subclass	Yes	No
	DE 38 51 038	10-22-88	Germany				
	WO 98/33119	7-30-98	PCT				

Examiner's Initials	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
	Microsoft Knowledge Base Article – Q125867, "understanding Win 16Mutex" 07/02/95 pages 1-2
	M. Pietrek; Direct Thunking in Windows 95, Dr. Dobb's Journal 1/96
	"Architektur von Bestriebssystemen", Wetterstein, Hanser Studien Bucher, 1984, page 54 ff
	Tananbaum, "Betriebssysteme – Entwurf und Realisierung" Teil 1, Prentice Hall International 1990 pages 31-32

Examiner:	Date Considered:		
*Examiner: Initial if citation considered, whether or not cit line through citation if not in conformance and not c communication to applicant.			